

**AMENDMENTS TO THE CLAIMS**

1-26. (Canceled)

27. (Previously Presented): An active matrix liquid crystal display apparatus, comprising:

    a pixel having a switching transistor, wherein the switching transistor includes a gate electrode, a source electrode, and a drain electrode that is connected to a pixel electrode;

    a data signal line connected to the source electrode;

    a gate signal line connected to the gate electrode; and

    a gate driver connected to the gate signal line, said gate driver including a first voltage source, a second voltage source, and a switch for selectively applying, in response to gate control signals, a reference potential, the first voltage, or the second voltage to the gate signal line, wherein the gate signal line is electrically connected to the gate electrode;

    a data driver connected to the data signal line, said data driver for selectively applying a data signal having a data signal voltage to the data signal line, wherein the data signal line is electrically connected to the source electrode; and

    a gate controller for selectively applying gate control signals to the gate driver;

    wherein the gate controller applies gate control signals that cause the gate driver to apply the first voltage to the gate signal line during the application of the data signal to the pixel electrode through the data signal line;

    wherein the gate controller applies gate control signals that cause the gate driver to apply the second voltage to the gate signal line after the application of the first signal voltage, but during the application of the data signal to the pixel electrode through the data signal line;

    wherein the gate controller applies gate control signals that cause the gate driver to apply the reference potential to the gate signal line after the application of the second signal voltage but during the application of the data signal to the pixel electrode through the data signal line;

    wherein the first voltage is greater than the data signal voltage and turns on the switching transistor;

    wherein the second voltage is near the data signal voltage; and

    wherein the reference voltage turns off the switching transistor.

28. (Previously Presented): The active matrix liquid crystal display apparatus as claimed in claim 27, wherein the gate signal line has a potential that drops from the first voltage to the second voltage over a period of time.

29. (Previously Presented): The active matrix liquid crystal display apparatus as claimed in claim 28, wherein the gate signal line potential drops exponentially over the period of time.

30. (Previously Presented): The active matrix liquid crystal display apparatus as claimed in claim 28, wherein the gate signal line potential drops linearly over the period of time.

31. (Previously Presented): The active matrix liquid crystal display apparatus as claimed in claim 28, wherein the gate signal line potential drops stepwise over the period of time.

32. (Previously Presented): The active matrix liquid crystal display apparatus as claimed in claim 27, wherein the first voltage is greater than the second voltage.

33. (Previously Presented): The active matrix liquid crystal display apparatus as claimed in claim 27, wherein the gate controller includes a timing controller.

34. (Previously Presented): The active matrix liquid crystal display apparatus as claimed in claim 27, wherein the gate controller includes analog switches that are controlled by a shift register.

35. (Previously Presented): The active matrix liquid crystal display apparatus as claimed in claim 27, wherein the gate signal line includes a distributed series resistance and a distributed capacitance.

36. (Previously Presented): The active matrix liquid crystal display apparatus as claimed in claim 27, wherein the first voltage is applied before the data signal is applied.

37. (Previously Presented): The active matrix liquid crystal display apparatus of claim 27, wherein second voltage is ground.

38-88. (Canceled)